#### High-Speed USB 2.0 (480 Mbps) DPDT Switches

# **General Description**

The LTC7227 is 2 to 1 port analog switches. Their wide bandwidth and low bit-to-bit skew allow them to pass high-speed differential signals with good signal integrity. Each switch is bidirectional and offers little or no attenuation of the high-speed signals at the outputs. Industry-leading advantages include a propagation delay of less than 250 ps, resulting from its low channel resistance and low I/O capacitance. Their high channel-to-channel crosstalk rejection results in minimal noise interference. Their bandwidth is wide enough to pass High-Speed USB 2.0 differential signals (480 Mbps).

#### **Features**

- Ron is Typically 6Ω at Vcc = 3.3 V
- Low Bit-to-Bit Skew: Typically 50 ps
- Low Crosstalk: -45 dB @ 250 MHz
- Low Current Consumption: 1.0 μA
- Near-Zero Propagation Delay: 250 ps
- Channel On-Capacitance: 3.5pF Typically
- VCC Operating Range: 1.65 V to 4.5 V
- >750 MHz Bandwidth (or Data Frequency)
- Available Packages: QFN1.8 × 1.4-10L and MSOP-10L

## **Applications**

- Differential Signal Data Routing
- USB 2.0 Signal Routing

#### Order Information

MODEL	PACKAGE	ORDERING NUMBER
LTCTCCC	QFN1.8×1.4-10L	LTC7227YFS10
LTC7222	MSOP-10L	LTC7227YV10



# **Pin Description**

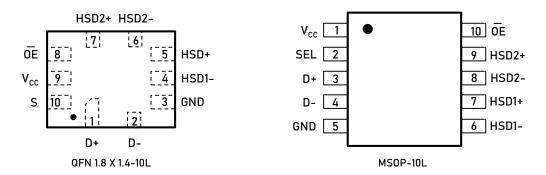


Figure 1. Top View

# Pin Function

Pin Name	Function
SEL	Select Input
ŌĒ	Output Enable
HSD1+,HSD1-,HSD2+,HSD2-,D+,D-	Data Ports

## Truth Table

<del>OE</del>	SEL	HSD1+,HSD1-	HSD2+,HSD2-
1	X	OFF	OFF
0	0	ON	0FF
0	1	0FF	ON



# **Absolute Maximum Ratings**

Symbol	Pin	Parameter	Value	Unit
V <sub>cc</sub>	V <sub>cc</sub>	Positive DC Supply Voltage	-0.5 to +6.0	٧
V	HSD1+,HSD1-,HSD2+,HSD2-		-0.5 to $ m V_{CC}$	- v
V <sub>IS</sub>	D+,D-	Analog Signal Voltage	-0.5 to +5.5	- v
V <sub>IN</sub>	OE,SEL	Control Input Voltage	-0.5 to $ m V_{CC}$	٧
I <sub>cc</sub>	V <sub>cc</sub>	Positive DC Supply Current	50	mA
T <sub>s</sub>		Storage Temperature	-65 to +150	°C
I <sub>IS_CON</sub>	HSD1+,HSD1-,HSD2+,HSD2- D+,D-	Analog Signal Continuous Current-Closed Switch	±100	mA
I <sub>IS_PK</sub>	HSD1+,HSD1-,HSD2+,HSD2- D+,D-	Analog Signal Continuous Current 10% Duty Cycle	±150	mA
I <sub>IN</sub>	ŌĒ	Control Input Current	±20	mA
	All pin	Human Body Model, JEDEC: JESD22-A114	>8	
ESD	Air Discharge	IEC 61000-4-2 System on	<b>&gt;</b> 15	kV
	Contact	USB Connector Pins D+, D-	>8	_
	Charged Device Model, JEDEC:	>2	_	

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

### RECOMMENDED OPERATING CONDITIONS

Symbol	Pins	Parameter	Min	Max	Unit
V <sub>cc</sub>		Positive DC Supply Voltage	1.65	4.5	V
V	HSD1+,HSD1-,HSD2+,HSD2-	- Analag Cignal Valtage	GND	V <sub>cc</sub>	· V
V <sub>IS</sub>	D+,D-	- Analog Signal Voltage	GND	4.5	· V
V <sub>IN</sub>	ŌĒ	Digital Select Input Voltage	GND	V <sub>cc</sub>	V
T <sub>A</sub>		Operating Temperature Range	-40	+85	°C

Minimum and maximum values are guaranteed through test or design across the Recommended Operating Conditions, where applicable. Typical values are listed for guidance only and are based on the particular conditions listed for section, where applicable. These conditions are valid for all values found in the characteristics tables unless otherwise specified in the test conditions.



## DC Electrical Characteristics

CONTROL INPUT (Typical: T = 25  $^{\circ}$ C, V<sub>CC</sub> = 3.3 V)

Complete	Symbol Pins P		Test	V 60	-40	11.2		
Symbol		Parameter Conditions		V <sub>cc</sub> (V)	Min	Тур	Max	Unit
V <sub>IH</sub>	0E	Control Input High Voltage		2.7 3.3	1.3 1.4	-	_	v
V <sub>v</sub>	ŌE	Control Input Low Voltage		2.7 3.3 4.2	1.6 -		0.4 0.4 0.5	V
I <sub>IN</sub>	 0E	Control Input Leakage Current	0≤V <sub>IS</sub> ≤V <sub>CC</sub>	1.65 – 4.5	-	-	±1.0	μΑ

#### SUPPLY AND LEAKAGE CURRENT (Typical: T = 25 °C, V<sub>CC</sub> = 3.3 V)

Complete	Dive	Barrandari	Test	V 00	-40°C to +85°C		
Symbol	Pins	Parameter	Conditions	V <sub>cc</sub> (V)	Min	Max	unit
I <sub>cc</sub>	V <sub>cc</sub>	Quiescent Supply Current	$V_{IS}$ = $V_{CC}$ or GND $I_{OUT}$ =0A	1.65 – 4.5	-	1.0	μΑ
I <sub>CCT</sub>	V <sub>cc</sub>	Increase in ICC per Control Voltage	V <sub>IN</sub> =2.6V	3.6	-	10	μΑ
I <sub>oz</sub>	HSD1+ HSD1- HSD2+ HSD2-	OFF Stage Leakage Current	0≤V <sub>IS</sub> ≤V <sub>CC</sub>	1.65 – 4.5	-	±1.0	μΑ
I <sub>OFF</sub>	D+,D-	Power OFF Leakage Current	0≤V <sub>IS</sub> ≤4.5V	0	-	±1.0	μΑ

#### HIGH SPEED ON RESISTANCE (Typical: T = 25 °C, $V_{CC}$ = 3.3 V)

Symbol Pins	Donomoton	Parameter Test Conditions	V 00	-4	Unit			
	Parameter		V <sub>cc</sub> (V)	Min	Тур	Max	Unit	
R <sub>on</sub>		On-Resistance	V <sub>IS</sub> = 0 V to 0.4 V, I <sub>ON</sub> =8 mA	3.3	-	6.0	10	Ω
R <sub>FLAT</sub>		On-Resistance Flatness	V <sub>IS</sub> = 0 V to 1.0 V, I <sub>ON</sub> =8 mA	3.3	-	0.5	-	Ω
$\triangle R_{0N}$		On-Resistance Matching	V <sub>IS</sub> = 0 V to 0.4 V, I <sub>ON</sub> =8 mA	3.3	-	0.2	-	Ω



TIMING/FREQUENCY (Typical: T = 25 °C,  $V_{CC}$  = 3.3V,  $R_L$  = 50 $\Omega$ ,  $C_L$  = 5 pF, f = 1MHz)

Complete	Dive	Darameter Test Conditions	Tank Canadikiana	Test Conditions V <sub>cc</sub> (V)	-40°C to +85°C			
Symbol	Pins	Parameter	rest Conditions		Min	Тур	Max	unit
t <sub>on</sub>	Closed to Open	Turn-ON Time		1.65- 4.5	-	14	30	ns
t <sub>OFF</sub>	Open to Closed	Turn-OFF Time		1.65- 4.5	-	10	20	ns
t <sub>BBM</sub>		Break-Before- Ma ke Delay	$V_{IS}$ = 0 V to $V_{CC}$ , $I_{ON}$ = 8 mA	1.65- 4.5	-	2.20 2.45 2.65	-	ns
BW		2dD Dandwidth	C <sub>L</sub> =5pF	20 / E	-	550	-	MII-
		-3dB Bandwidth	C <sub>L</sub> =0pF	3.0- 4.5	-	900	-	- MHz

ISOLATION (Typical: T = 25 °C,  $V_{CC}$  = 3.3V,  $R_L$  = 50 $\Omega$ ,  $C_L$  = 5 pF, f = 1 MHz)

Symbol Pin	Dia	Dawawatan	Test Conditions	V (\( \)	-40°C to +85°C			unit
	Parameter	rest conditions	V <sub>cc</sub> (V)	Min	Тур	Max	unit	
O <sub>IRR</sub>	Open	OFF-Isolation	f=250 MHz	1.65- 4.5	-	-30	-	dB
$\mathbf{X}_{TALK}$	HSD1+ to HSD1-	Non-Adjacent Channel Crosstalk	f=250 MHz	1.65- 4.5	-	-45	-	dB

CAPACITANCE (Typical: T =  $25^{\circ}$ C,  $V_{CC}$  = 3.3 V,  $R_{L}$  =  $50\Omega$ ,  $C_{L}$  = 5 pF, f = 1 MHz)

o										
Cymbal	Pins	Pins Parameter	Test Conditions	V (A)	-40°C to +85°C			unit		
Symbol	Parameter Test Conditions	V <sub>cc</sub> (V)	Min	Тур	Max	unit				
C <sub>IN</sub>	0E	Control Pin Input Capacitance		0		1.8		pF		
C <sub>ON</sub>	D+ to HSD1+ or HSD2+	ON Capacitance	ŌE= 0 V	3.3		4.0		pF		
C <sub>OFF</sub>	HSD2+, HSD2-	OFF Capacitance	VIS = 3.3 V OE=3.3 V	3.3		2.2		pF		

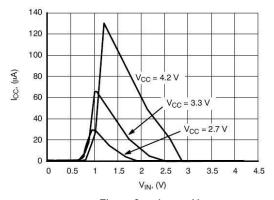
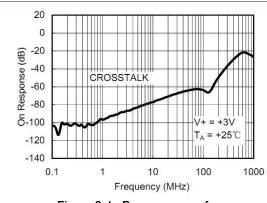


Figure2-a.  $\rm I_{CC}$  vs.  $\rm V_{IN}$ 





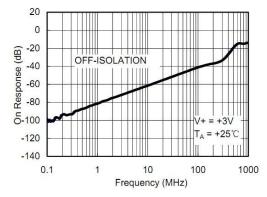
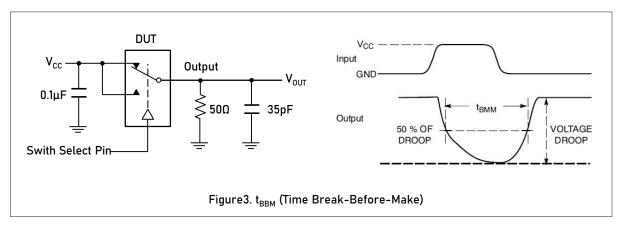
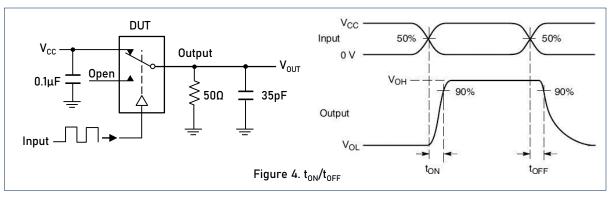
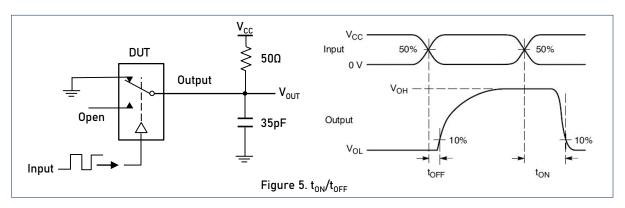


Figure 2-b. Response vs. frequency

Figure 2-c. Response vs. frequency

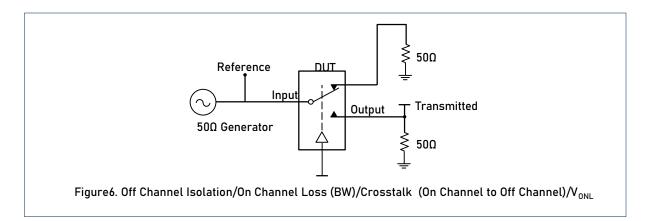








### High-Speed USB 2.0 (480 Mbps) DPDT Switches



Channel switch control/s test socket is normalized. Off isolation is measured across an off channel. On loss is the bandwidth of an On switch. VISO, Bandwidth and VONL are independent of the input signal direction.

VISO = Off Channel Isolation = 20 Log 
$$\left(\frac{Vou\tau}{V_{\rm IN}}\right)$$
 for VIN at 100 kHz

VONL = On Channel Loss = 20 Log 
$$\left(\frac{Vou\tau}{V_{IN}}\right)$$
 for VIN at 100 kHz

Bandwidth (BW) = the frequency 3 dB below  $V_{ONL}$ 

 $V_{\text{CT}}$  = Use  $V_{\text{ISO}}$  setup and test to all other switch analog input/outputs terminated with  $50\Omega$ 



Typical Performance Curves  $T_A$  = +25  $^{\circ}$ C, Unless Otherwise Specified (Continued)

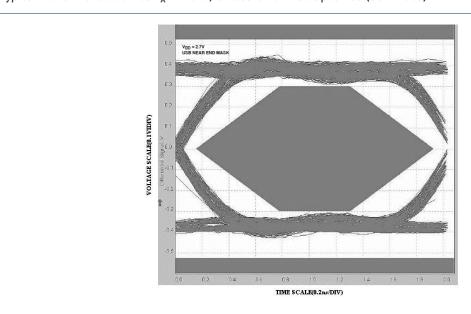


Figure 7. EYE PATTERN: 480Mbps WITH USB SWITCHES IN THE SIGNAL PATH

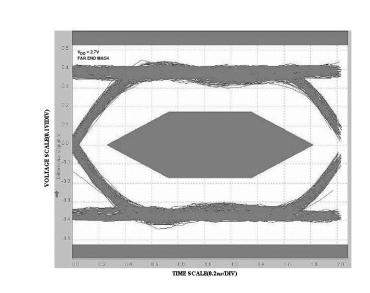
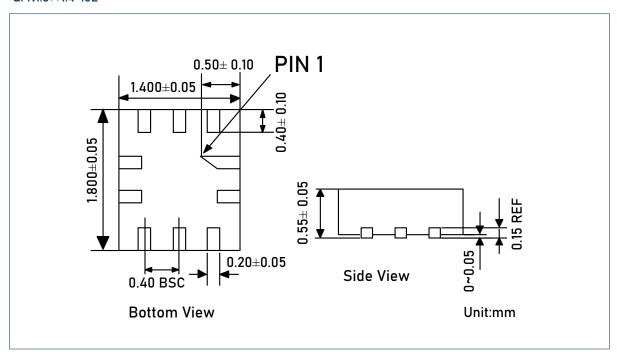


Figure 8. EYE PATTERN: 480Mbps WITH USB SWITCHES IN THE SIGNAL PATH



# Package Dimension

#### QFN1.8×1.4-10L



#### MSOP-10L

