General Description

The LTA8261, LTA8262 and LTA8264 (LTA826x) are a family of micro-power, 48 V wide supply voltage, rail-to-rail output operational amplifiers capable of operating on supplies ranging from +4.5 V to +48 V. This new generation of high-voltage CMOS operational amplifiers, in conjunction with the LTA829x, LTA828x and LTA827x, provide a family of bandwidth, noise, and power options to meet the needs of a wide variety of applications. The LTA826x devices offer outstanding dc precision and ac performance, including low offset (±2 mV maximum), low offset drift (±2 µV/°C typically), 1.1 MHz bandwidth, and 22 nV/√Hz input voltage noise density at 1 kHz. Unique features such as differential input-voltage range to the negative supply rail, high output current (±45 mA), high capacitive load drive of up to 1 nF, and high slew rate (0.8 V/µs) make the LTA826x high-performance operational amplifiers for high-voltage industrial applications.

The robust design of the LTA826x family provides ease-of-use to the circuit designer: integrated RF/EMI rejection filter, no phase reversal in overdrive conditions, and high electro-static discharge (ESD) protection. The LTA826x are optimized for operation at voltages from +4.5 V (±2.25 V) to +48 V (±24 V) over the extended temperature range of −40 °C to +125 °C.

Features and Benefits

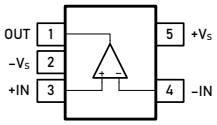
- Wide Supply: ±2.25 V to ±24 V, 4.5 V to 48 V
- Low Offset Voltage: ±2 mV Maximum
- Low Offset Voltage Drift: ±2 µV/°C
- High Common-Mode Rejection: 112 dB
- Gain Bandwidth: 1.1 MHz
- Slew Rate: 0.8 V/µs
- Low Noise: 22 nV/√Hz at 1 kHz
- Low Quiescent Current: 140 µA per amplifier
- Low Bias Current: ±10 pA
- Rail-to-Rail Output

Applications

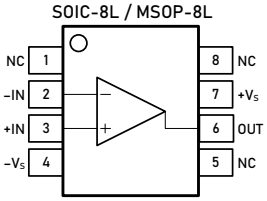
- Tracking Amplifier in Power Modules
- Power Delivery: UPS, Server, and Merchant Network Power
- High-Side and Low-Side Current Sensing
- Transducer Amplifiers
- Battery-Powered Instruments
- Test and Measurement Equipment
- Multiplexed Data-Acquisition Systems
- Programmable Logic Controllers

Pin Configuration (Top View)

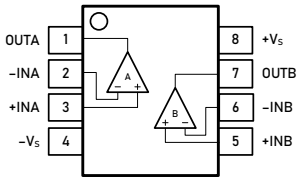
LTA8261
SOT23-5L



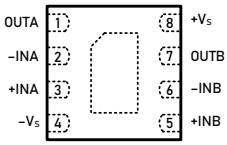
LTA8261
SOIC-8L / MSOP-8L



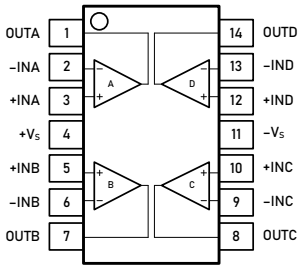
LTA8262
SOIC-8L / MSOP-8L



LTA8262
DFN3×3-8L



LTA8264
SOIC-14L / TSSOP-14L



Pin Description

Symbol	Description
–IN	Inverting input of the amplifier. The voltage range is from V_{S-} to V_{S+} – 1.5 V.
+IN	Non-inverting input of the amplifier. This pin has the same voltage range as –IN.
$+V_S$	Positive power supply. The voltage is from 4.5 V to 48 V. Split supplies are possible as long as the voltage between V_{S+} and V_{S-} is from 4.5 V to 48 V.
$-V_S$	Negative power supply. It is normally tied to ground. It can also be tied to a voltage other than ground as long as the voltage between V_{S+} and V_{S-} is from 4.5 V to 48 V.
OUT	Amplifier output.
NC	No connection

Ordering Information (1)

Type Number	Package Name	Package Quantity	Eco Class (2)	Marking Code (3)
LTA8261XT5/R6	SOT23-5L	Tape and Reel, 3 000	Green (RoHS & no Sb/Br)	H61
LTA8261XS8/R8	SOIC-8L	Tape and Reel, 4 000	Green (RoHS & no Sb/Br)	HV-61
LTA8261XV8/R6	MSOP-8L	Tape and Reel, 3 000	Green (RoHS & no Sb/Br)	HV61
LTA8262XS8/R8	SOIC-8L	Tape and Reel, 4 000	Green (RoHS & no Sb/Br)	HV-62
LTA8262XV8/R6	MSOP-8L	Tape and Reel, 3 000	Green (RoHS & no Sb/Br)	HV62
LTA8262XF8/R6	DFN3x3-8L	Tape and Reel, 3 000	Green (RoHS & no Sb/Br)	HV62
LTA8264XS14/R5	SOIC-14L	Tape and Reel, 2 500	Green (RoHS & no Sb/Br)	HV-64
LTA8264XT14/R6	TSSOP-14L	Tape and Reel, 3 000	Green (RoHS & no Sb/Br)	HV-64

(1) Please contact to your Linearin representative for the latest availability information and product content details.

(2) Eco Class - The planned eco-friendly classification: Pb-Free (RoHS) or Green (RoHS & Halogen Free).

(3) There may be multiple device markings, a varied marking character of "x" , or additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.

Limiting Value – In accordance with the Absolute Maximum Rating System (IEC 60134).

Parameter	Absolute Maximum Rating
Supply Voltage, V_{S+} to V_{S-}	60 V
Signal Input Terminals: Voltage, Current	$-V_S$ – 0.3 V to $+V_S$ + 0.3 V, ±10 mA
Output Short-Circuit	Continuous
Storage Temperature Range, T_{stg}	–65 to +150 ℃
Junction Temperature, T_J	150 ℃
Lead Temperature Range (Soldering 10 sec)	260 ℃

ESD Rating

Parameter	Item	Value	Unit
Electrostatic Discharge Voltage	Human body model (HBM), per MIL-STD-883J / Method 3015.9 (1)	2 000	V
	Charged device model (CDM), per ESDA/JEDEC JS-002-2014 (2)	2 000	

(1) JEDEC document JEP155 states that 500-V HBM allows safe manufacturing with a standard ESD control process. Manufacturing with less than 500-V HBM is possible if necessary precautions are taken.

(2) JEDEC document JEP157 states that 250-V CDM allows safe manufacturing with a standard ESD control process. Manufacturing with less than 250-V CDM is possible if necessary precautions are taken.

Electrical Characteristics

V_S = 4.5 V to 48 V, T_A = +25 °C, V_{CM} = V_{OUT} = V_S /2, and R_L = 10 kΩ connected to V_S /2, unless otherwise noted. Boldface limits apply over the specified temperature range, T_A = −40 °C to +125 °C.

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
OFFSET VOLTAGE						
Input offset voltage	V_{OS}			±0.6	±2	mV
Offset voltage drift	V_{OS} TC	T_A = −40 to +125 °C		±2		µV/°C
Power supply rejection ratio	PSRR	V_S = 4.5 to 48 V, V_{CM} = 0.1 V		5		µV/V
		T_A = −40 to +125 °C		10		
INPUT BIAS CURRENT						
Input bias current	I_B			10		pA
		T_A = −40 to +85 °C		150		
		T_A = −40 to +125 °C		600		
Input offset current	I_{OS}			5		pA
NOISE						
Input voltage noise	V_n	f = 0.1 to 10 Hz		5		μV_{P-P}
Input voltage noise density	e_n	f = 1 kHz		22		nV/√Hz
Input current noise density	I_n	f = 1 kHz		5		fA/√Hz
INPUT VOLTAGE						
Common-mode voltage range	V_{CM}		$-V_S$		$+V_S$−1.5	V
Common-mode rejection ratio	CMRR	V_S = 40 V, V_{CM} = 0 to 38.5 V		112		dB
		V_{CM} = 0.1 to 38 V, T_A = −40 to +125 °C		100		
		V_S = 5 V, V_{CM} = 0 to 3.5 V		93		
		V_{CM} = 0.1 to 3 V, T_A = −40 to +125 °C		82		
INPUT IMPEDANCE						
Input capacitance	C_{IN}	Differential		2		pF
		Common mode		3.5		
OPEN-LOOP GAIN						
Open-loop voltage gain	A_{VOL}	V_S = 40 V, V_O = 0.1 to 39.9 V		126		dB
		T_A = −40 to +125 °C		118		
		V_S = 5 V, V_O = 0.1 to 4.9 V		116		
		T_A = −40 to +125 °C		108		
FREQUENCY RESPONSE						
Gain bandwidth product	GBW			1.1		MHz
Slew rate	SR	V_S = 40 V, G = +1, 10 V step		0.8		V/µs
Total harmonic distortion + noise	THD+N	G = +1, f = 1 kHz, V_O = 3 V_{RMS}		0.0006		%
Settling time	t_S	To 0.1%, V_S = 40 V, G = +1, 5 V step		16		µs
		To 0.01%, V_S = 40 V, G = +1, 5 V step		22		
Overload recovery time	t_{OR}	V_{IN} × Gain > V_S		2		µs

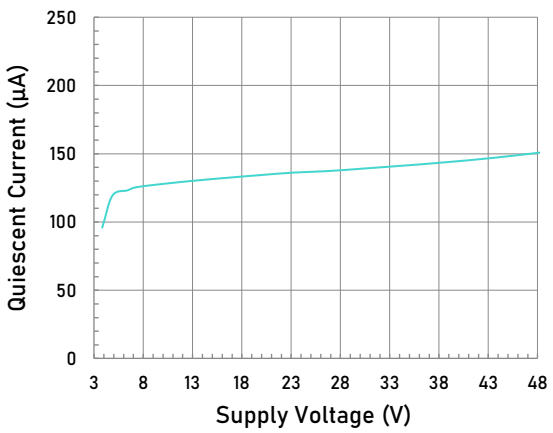
Electrical Characteristics (continued)

V_S = 4 V to 48 V, T_A = +25 °C, V_{CM} = V_{OUT} = V_S /2, and R_L = 10 kΩ connected to V_S /2, unless otherwise noted. Boldface limits apply over the specified temperature range, T_A = −40 °C to +125 °C.

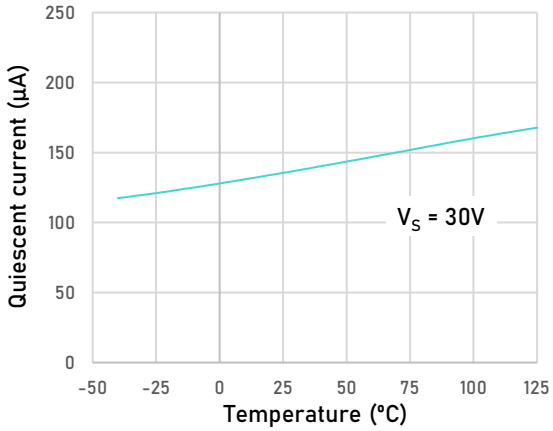
Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
OUTPUT						
High output voltage swing	V_{OH}	V_S = ±20 V, R_L = 10 kΩ		$+V_S$−100		mV
		V_S = ±20 V, R_L = 2 kΩ		$+V_S$−270		
Low output voltage swing	V_{OL}	V_S = ±20 V, R_L = 10 kΩ		$-V_S$+60		mV
		V_S = ±20 V, R_L = 2 kΩ		$-V_S$+250		
Short-circuit current	I_{SC}			±45		mA
POWER SUPPLY						
Operating supply voltage	V_S	T_A = −40 to +125 °C	4.5		48	V
Quiescent current (per amplifier)	I_Q	V_S = 5 V		122		µA
		V_S = 40 V		140		
THERMAL CHARACTERISTICS						
Operating temperature range	T_A		−40		+125	°C
Package Thermal Resistance	$θ_{JA}$	SOT23-5L		190		°C/W
		MSOP-8L		201		
		SOIC-8L		125		
		TSSOP-14L		112		
		SOIC-14L		115		

Typical Performance Characteristics

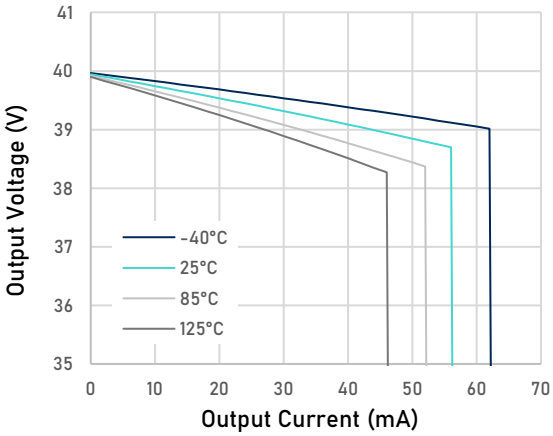
At T_A = +25 °C, V_{CM} = V_S /2, and R_L = 10 kΩ connected to V_S /2, unless otherwise noted.



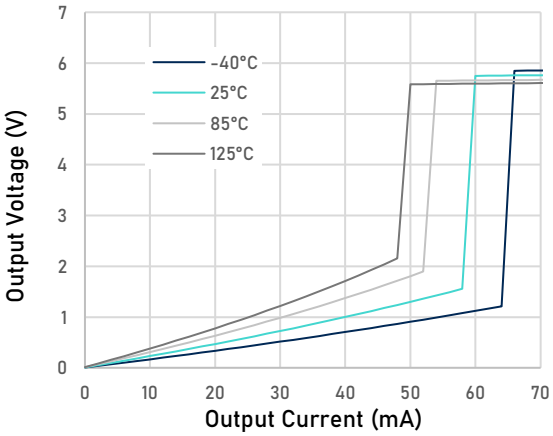
Quiescent Current as a function of Supply Voltage



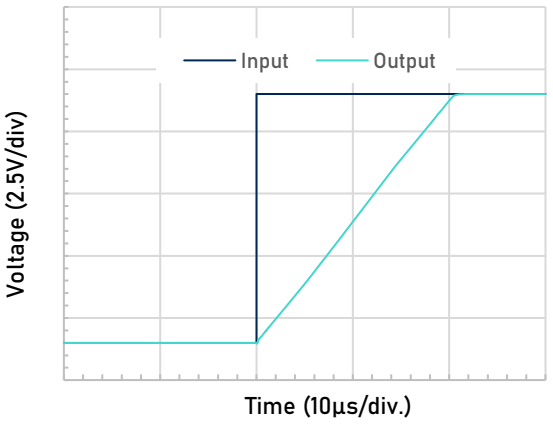
Quiescent Current as a function of Temperature



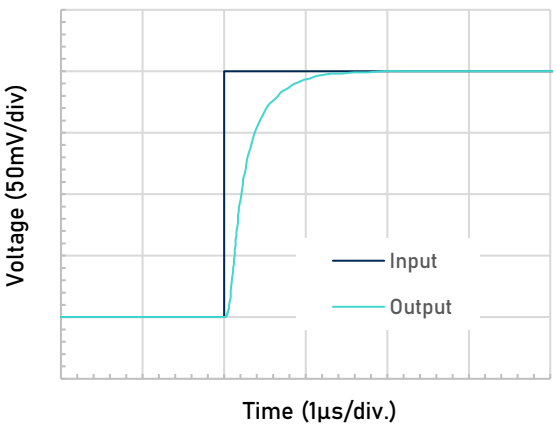
Output Voltage Swing as a function of Output Current (Sourcing, V_S = 40 V)



Output Voltage Swing as a function of Output Current (Sinking, V_S = 40 V)



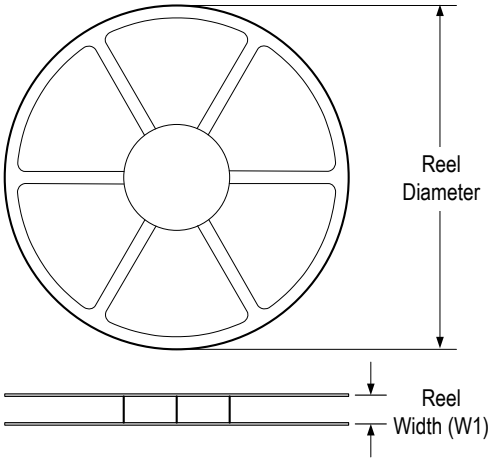
Large-Signal Step Response(Failing)



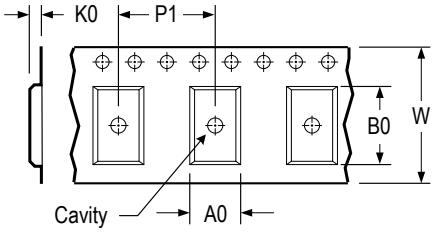
Small-Signal Step Response

Tape and Reel Information

REEL DIMENSIONS

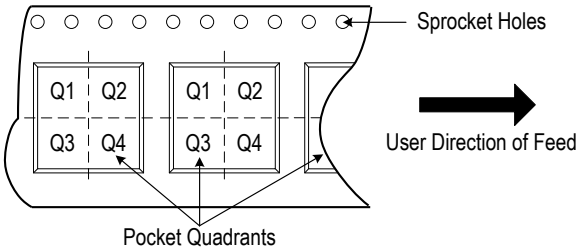


TAPE DIMENSIONS



A0	Dimension designed to accommodate the component width
B0	Dimension designed to accommodate the component length
K0	Dimension designed to accommodate the component thickness
W	Overall width of the carrier tape
P1	Pitch between successive cavity centers

QUADRANT ASSIGNMENTS FOR PIN 1 ORIETATION IN TAPE

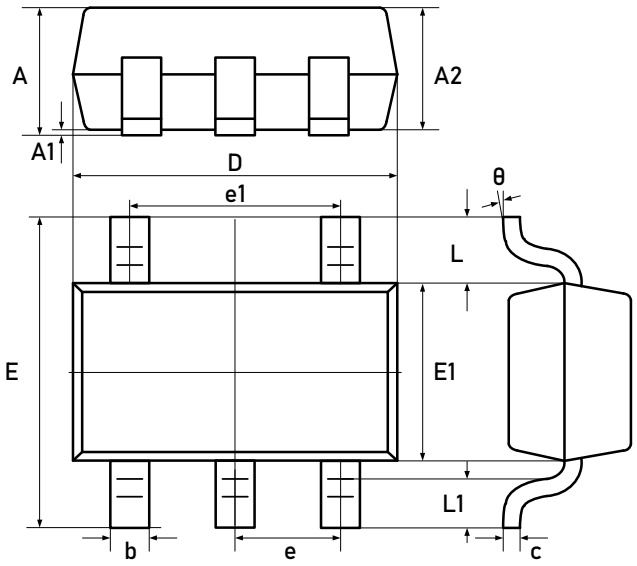


* All dimensions are nominal

Device	Package Type	Pins	SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin 1 Quadrant
LTA8261XT5/R6	SOT23	5	3 000	178	9.0	3.3	3.2	1.5	4.0	8.0	Q3

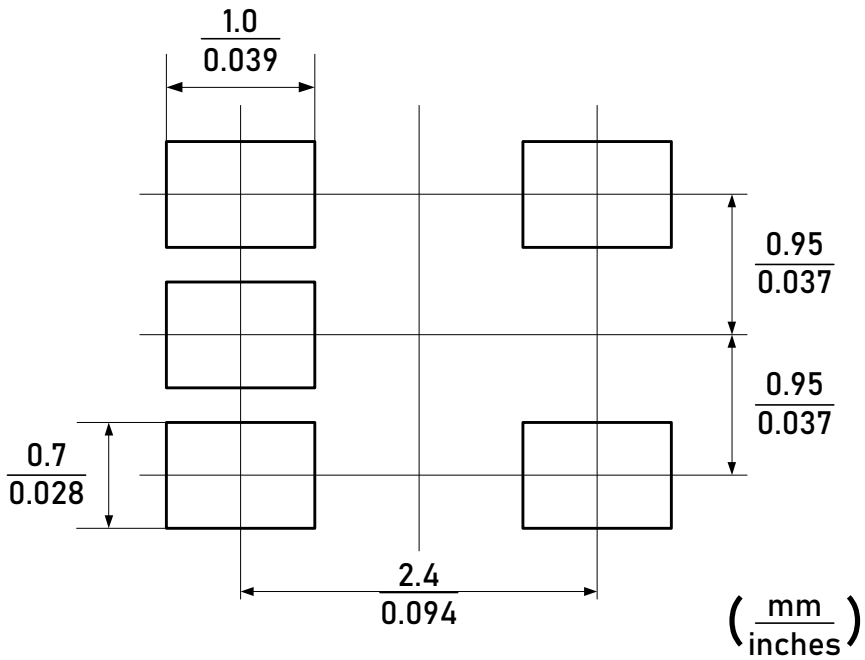
Package Outlines

DIMENSIONS, SOT23-5L



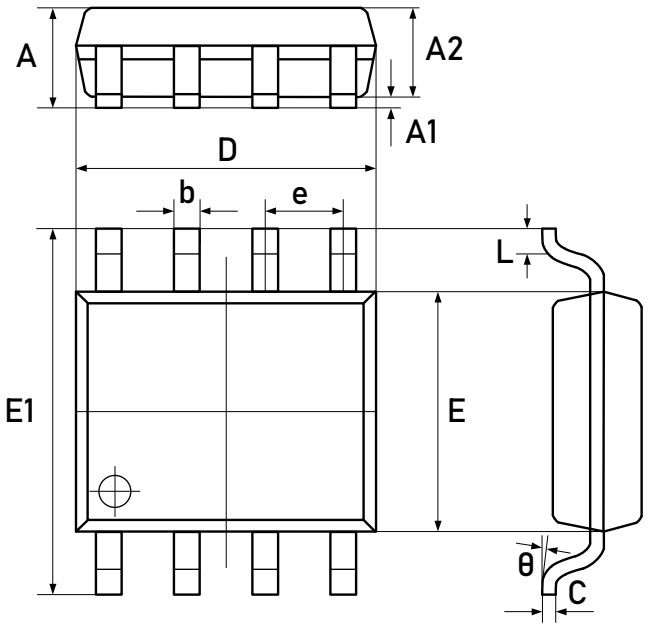
Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	-	1.25	-	0.049
A1	0.04	0.10	0.002	0.004
A2	1.00	1.20	0.039	0.047
b	0.33	0.41	0.013	0.016
c	0.15	0.19	0.006	0.007
D	2.820	3.02	0.111	0.119
E1	1.50	1.70	0.059	0.067
E	2.60	3.00	0.102	0.118
e	0.95 BSC		0.037 BSC	
e1	1.90 BSC		0.075 BSC	
L	0.60 REF		0.024 REF	
L1	0.30	0.60	0.012	0.024
θ	0°	8°	0°	8°

RECOMMENDED SOLDERING FOOTPRINT, SOT23-5L



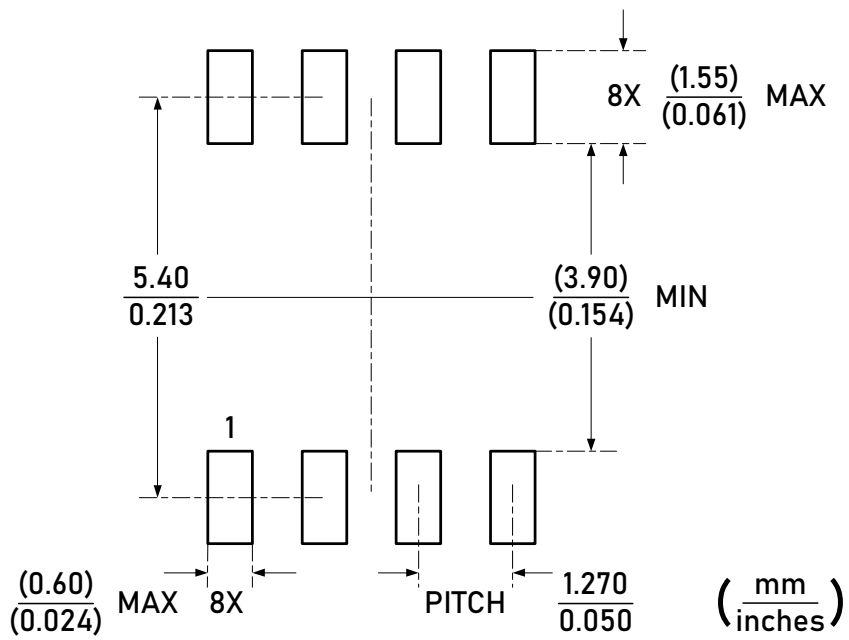
Package Outlines (continued)

DIMENSIONS, SOIC-8L



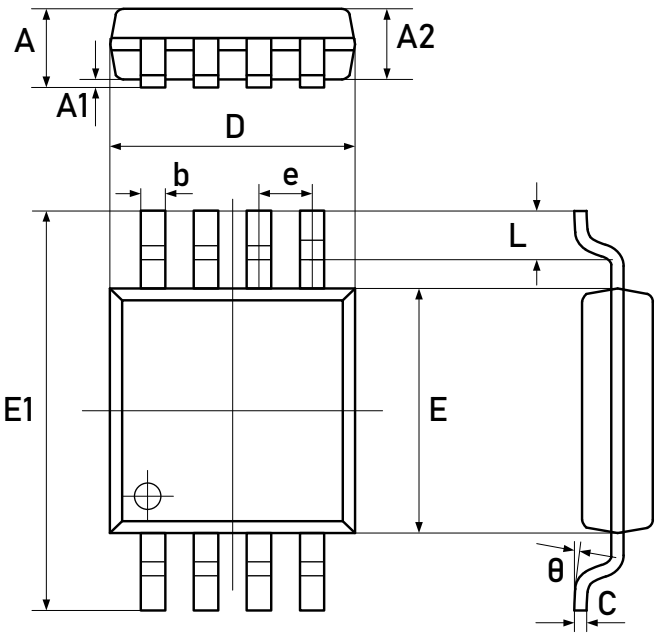
Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.370	1.670	0.054	0.066
A1	0.070	0.170	0.003	0.007
A2	1.300	1.500	0.051	0.059
b	0.306	0.506	0.012	0.020
C	0.203 TYP.		0.008 TYP.	
D	4.700	5.100	0.185	0.201
E	3.820	4.020	0.150	0.158
E1	5.800	6.200	0.228	0.244
e	1.270 TYP.		0.050 TYP.	
L	0.450	0.750	0.018	0.030
θ	0°	8°	0°	8°

RECOMMENDED SOLDERING FOOTPRINT, SOIC-8L



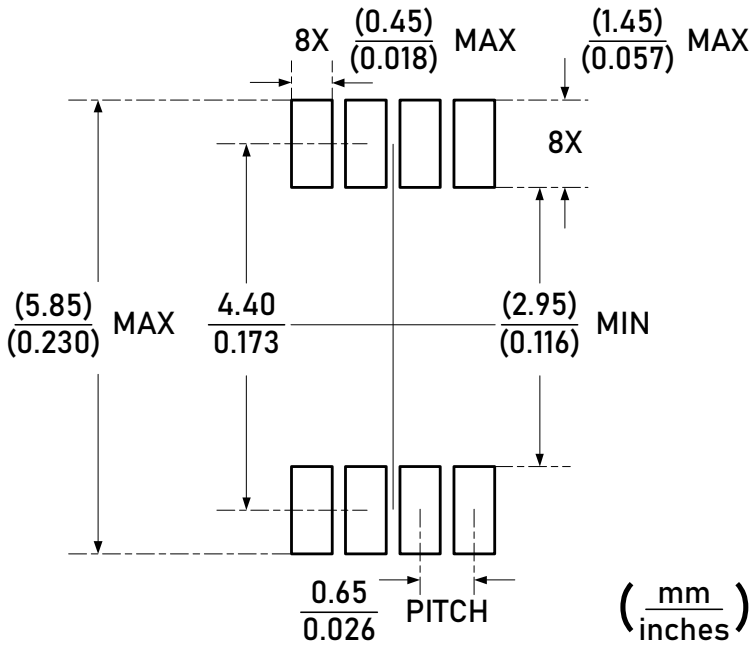
Package Outlines (continued)

DIMENSIONS, MSOP-8L



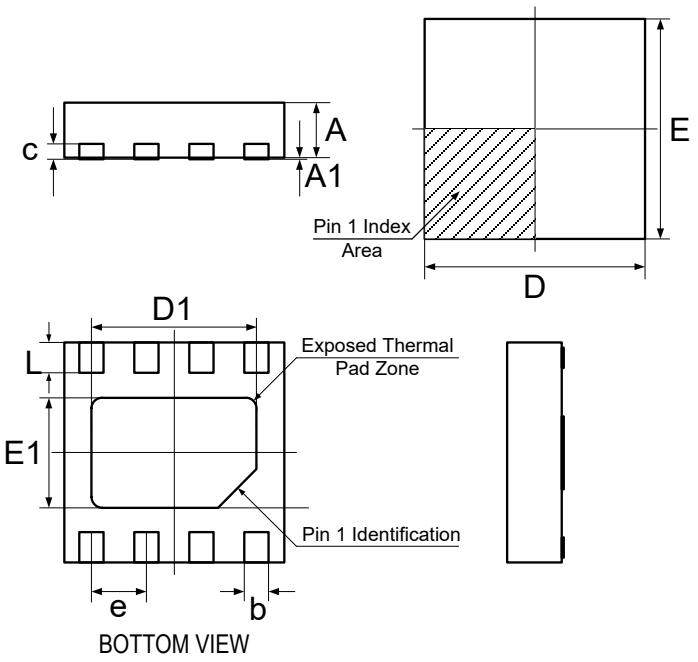
Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.800	1.100	0.031	0.043
A1	0.050	0.150	0.002	0.006
A2	0.750	0.950	0.030	0.037
b	0.290	0.380	0.011	0.015
C	0.150	0.200	0.006	0.008
D	2.900	3.100	0.114	0.122
E	2.900	3.100	0.114	0.122
E1	4.700	5.100	0.185	0.201
e	0.650 TYP.		0.026 TYP.	
L	0.400	0.700	0.016	0.028
θ	0°	8°	0°	8°

RECOMMENDED SOLDERING FOOTPRINT, MSOP-8L



Package Outlines (continued)

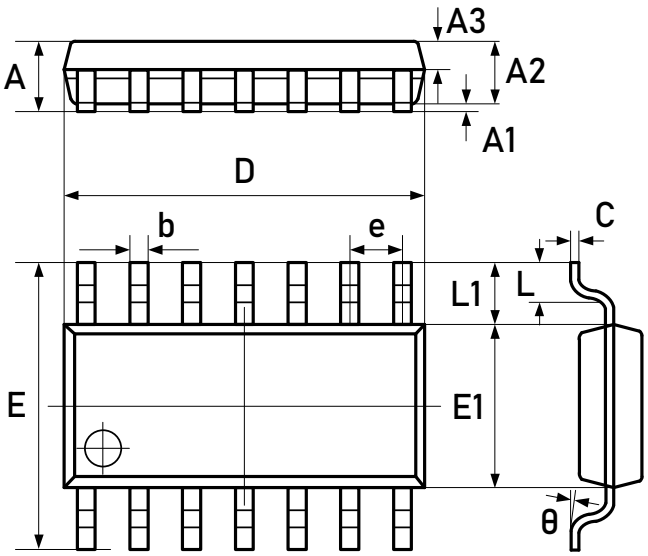
DIMENSIONS, DFN3x3-8L



Symbol	Millimeters		
	Min.	Nom.	Max.
A	0.70	0.75	0.80
A1	-	0.02	0.05
b	0.255	0.28	0.305
c	0.19	0.21	0.23
D	2.90	3.00	3.10
D1	2.25	2.30	2.35
E	2.90	3.00	3.10
E1	1.45	1.50	1.55
e	0.625	0.65	0.675
L	0.25	0.30	0.35

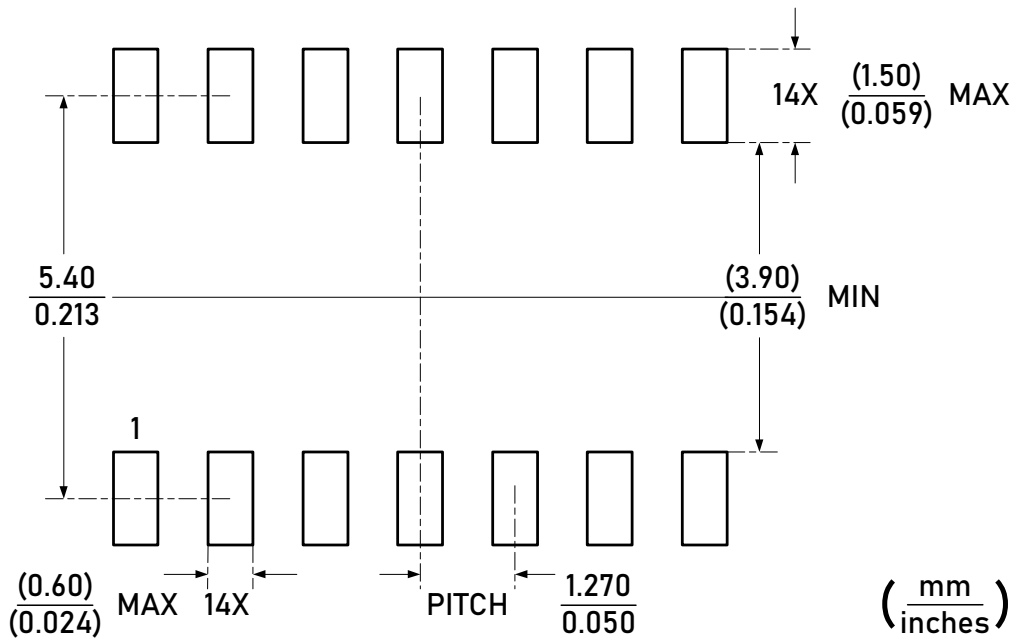
Package Outlines (continued)

DIMENSIONS, SOIC-14L



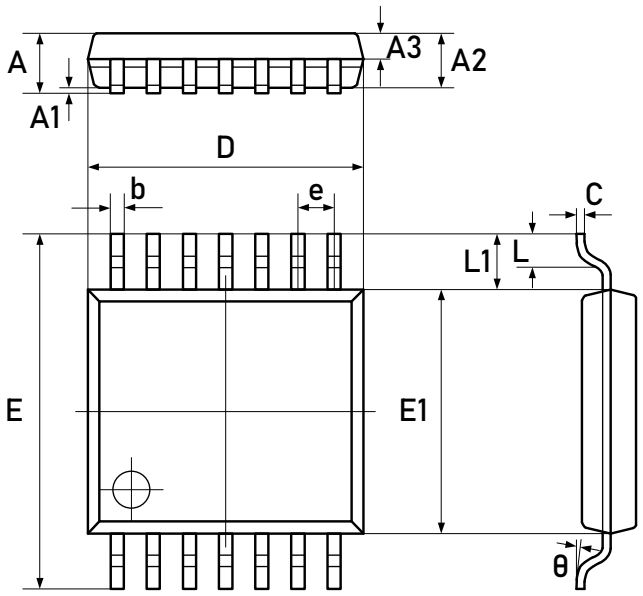
Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.450	1.850	0.057	0.073
A1	0.100	0.300	0.004	0.012
A2	1.350	1.550	0.053	0.061
A3	0.550	0.750	0.022	0.030
b	0.406 TYP.		0.016 TYP.	
C	0.203 TYP.		0.008 TYP.	
D	8.630	8.830	0.340	0.348
E	5.840	6.240	0.230	0.246
E1	3.850	4.050	0.152	0.159
e	1.270 TYP.		0.050 TYP.	
L1	1.040 REF.		0.041 REF.	
L	0.350	0.750	0.014	0.030
θ	2°	8°	2°	8°

RECOMMENDED SOLDERING FOOTPRINT, SOIC-14L



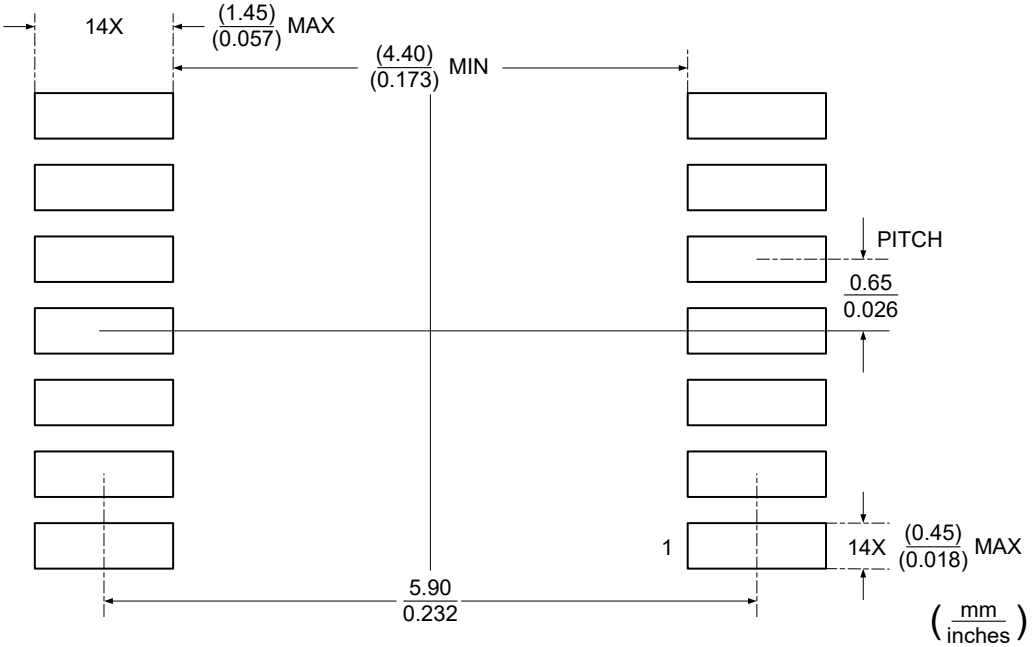
Package Outlines (continued)

DIMENSIONS, TSSOP-14L



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	-	1.200	-	0.047
A1	0.050	0.150	0.002	0.006
A2	0.900	1.050	0.035	0.041
A3	0.390	0.490	0.015	0.019
b	0.200	0.290	0.008	0.011
C	0.130	0.180	0.005	0.007
D	4.860	5.060	0.191	0.199
E	6.200	6.600	0.244	0.260
E1	4.300	4.500	0.169	0.177
e	0.650 TYP.		0.026 TYP.	
L1	1.000 REF.		0.039 REF.	
L	0.450	0.750	0.018	0.030
θ	0°	8°	0°	8°

RECOMMENDED SOLDERING FOOTPRINT, SOIC-14L



Important Notice

Linearin is a global fabless semiconductor company specializing in advanced high-performance high-quality analog/mixed-signal IC products and sensor solutions. The company is devoted to the innovation of high performance, analog-intensive sensor front-end products and modular sensor solutions, applied in multi-market of medical & wearable devices, smart home, sensing of IoT, intelligent industrial & smart factory (industrie 4.0), and automotives. Linearin's product families include widely-used standard catalog products, solution-based application specific standard products (ASSPs) and sensor modules that help customers achieve faster time-to-market products. Go to http://www.linearin.com for a complete list of Linearin product families.

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